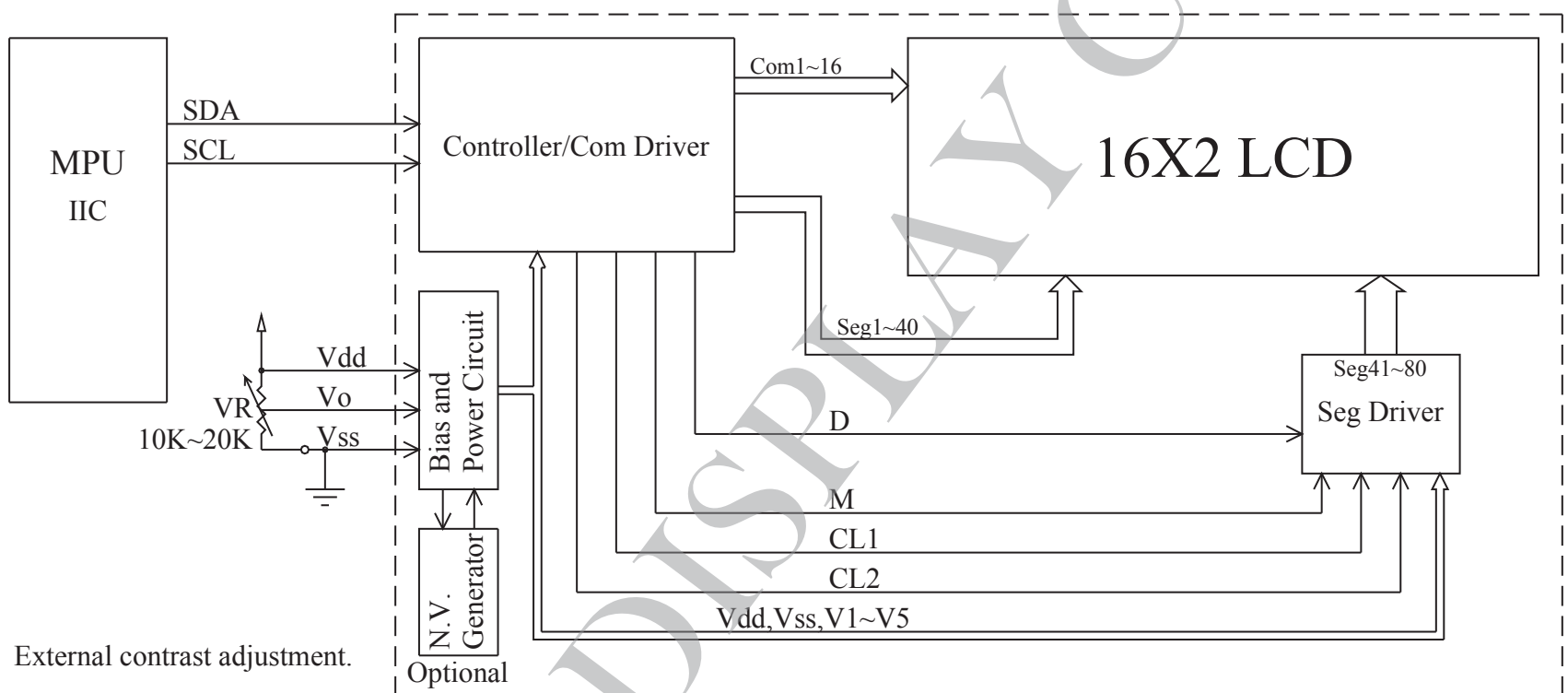
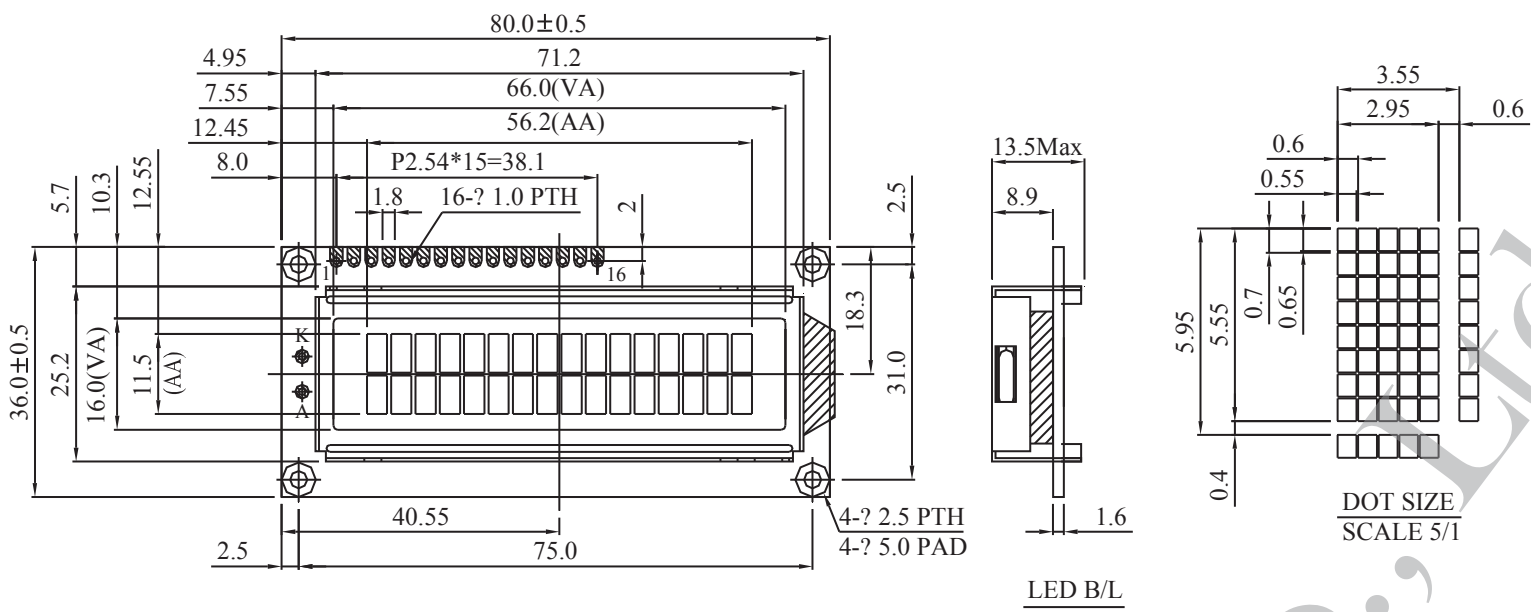


Contour Drawing & Block Diagram



Character located	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
DDRAM address	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
DDRAM address	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F

Interface Pin Function

Pin No.	Symbol	Level	Description
1	V _{SS}	0V	Ground
2	V _{DD}	5.0V	Supply Voltage for logic
3	VO	(Variable)	Operating voltage for LCD
4	NC	—	No connection
5	NC	—	No connection
6	NC	—	No connection
7	SA0	H/L	In IIC interface ,DB1(SA1) and DB0(SA0) are used for Slave address, must be connect to VDD or VSS
8	SA1	H/L	
9	NC	—	No connection
10	NC	—	No connection
11	NC	—	No connection
12	CSB	H/L	In IIC serial mode, used as chip selection input. When CSB = "Low", selected. When CSB = "High", not selected. (Low access enable)
13	SDA	H/L	serial input data
14	SCL	H/L	serial clock input
15	A	—	LED +
16	K	—	LED -

PIN NO.	SYMBOL
1	V _{SS}
2	V _{DD}
3	Vo
4	NC
5	NC
6	NC
7	SA0
8	SA1
9	NC
10	NC
11	NC
12	CSB
13	SDA
14	SCL
15	A
16	K